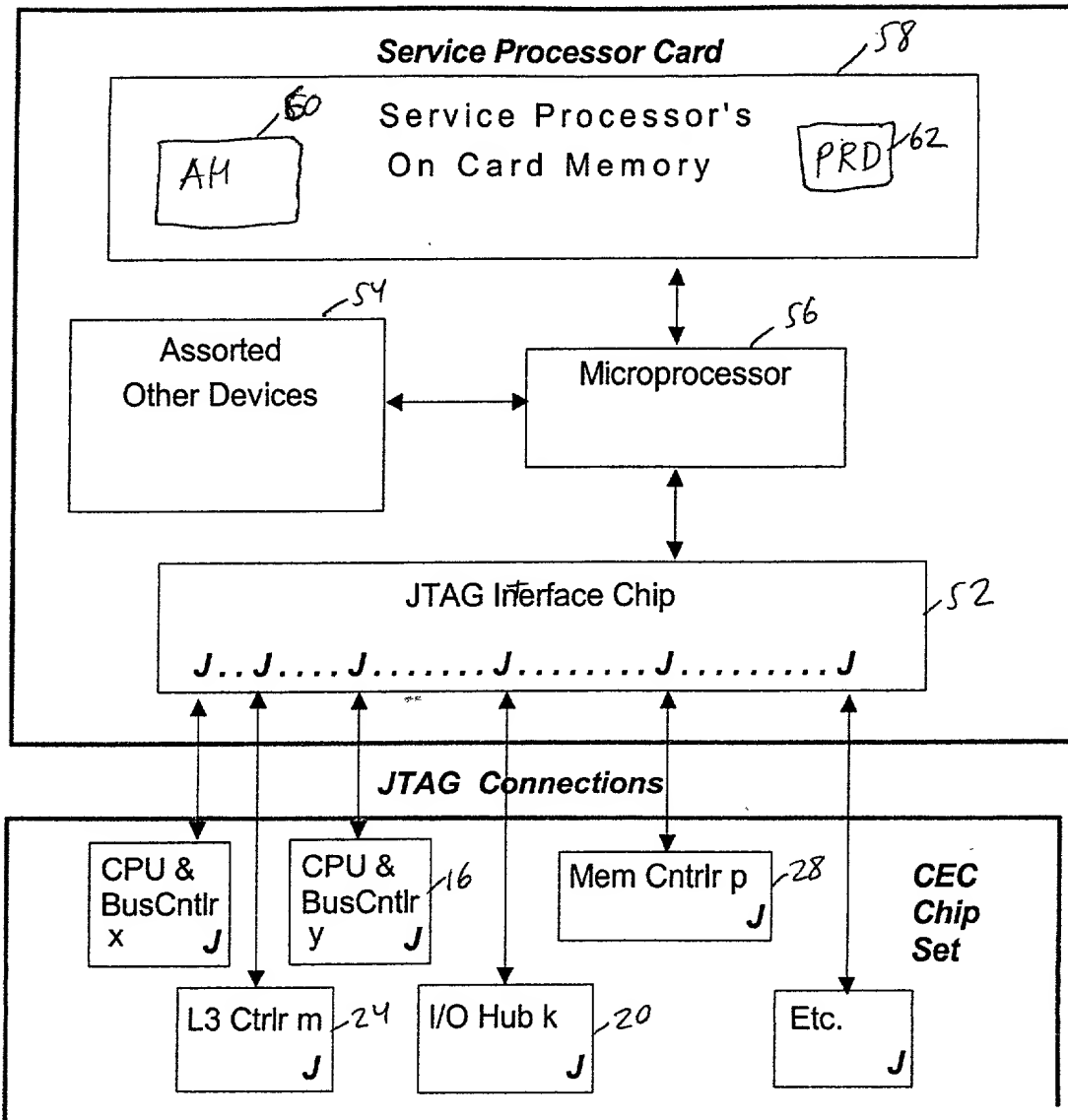


Figure 1

817
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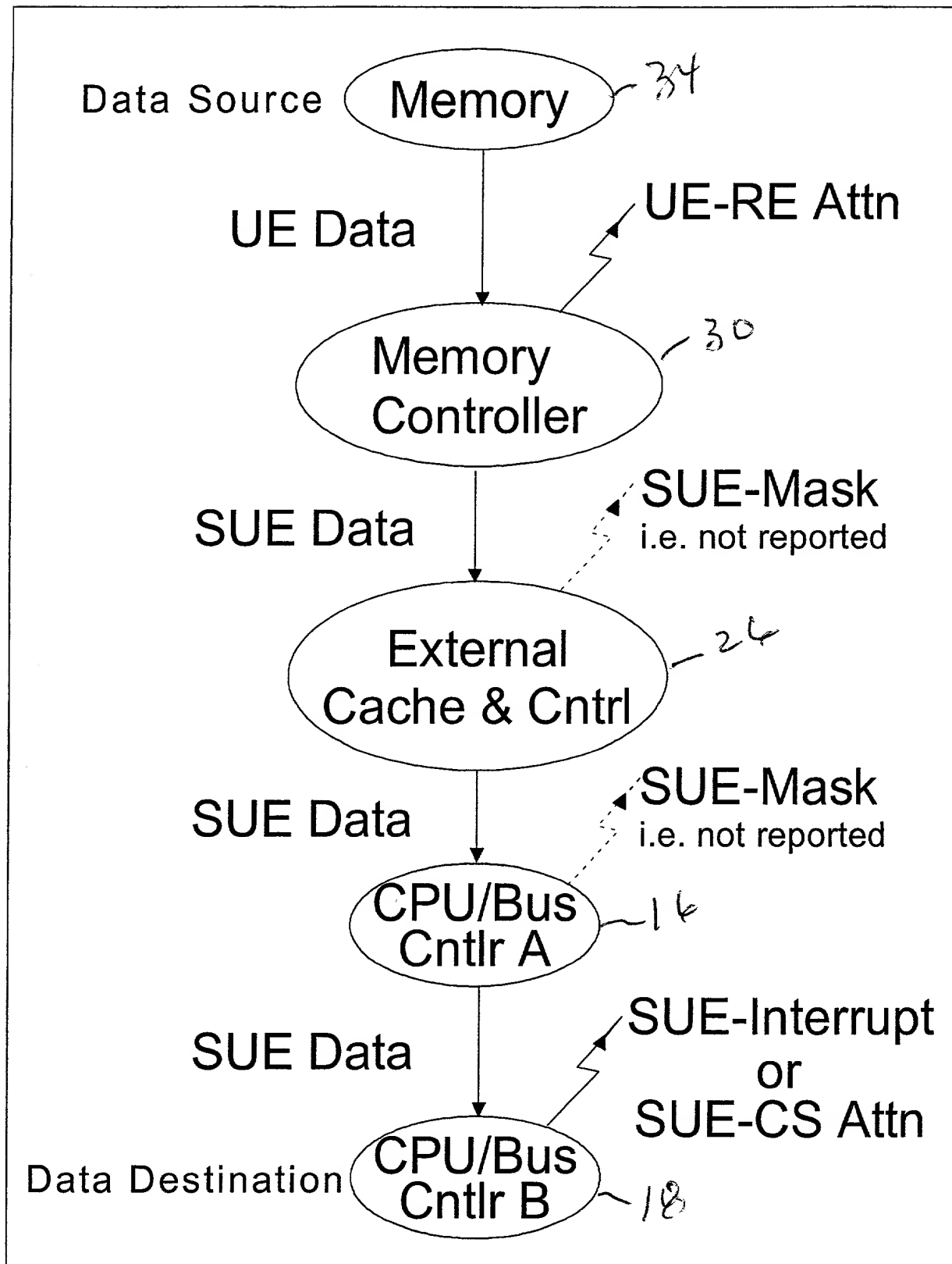
50



J = JTAG serial interface between Service Processor & chips in CEC

Figure 2

Figure 3



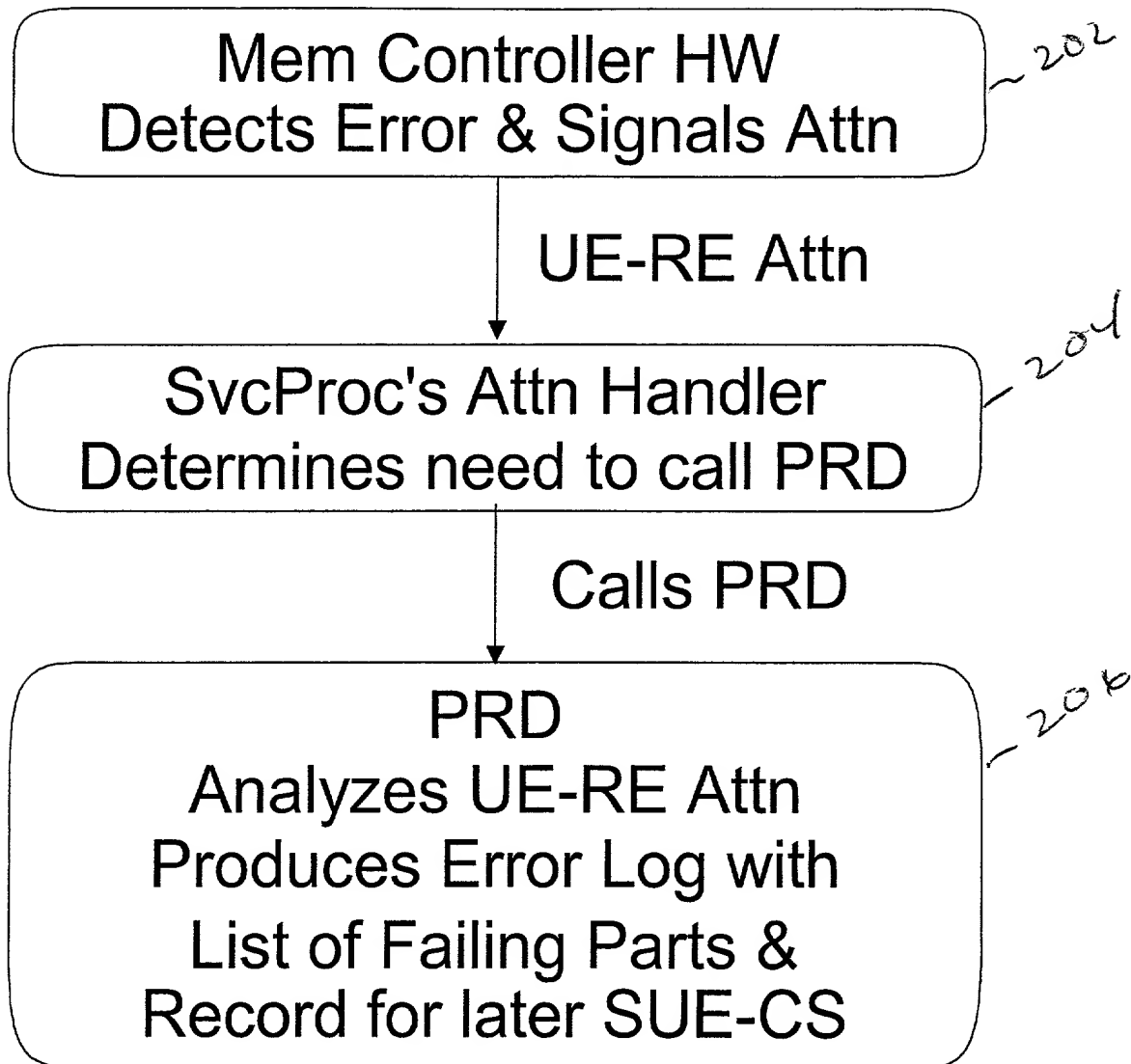


Figure 4

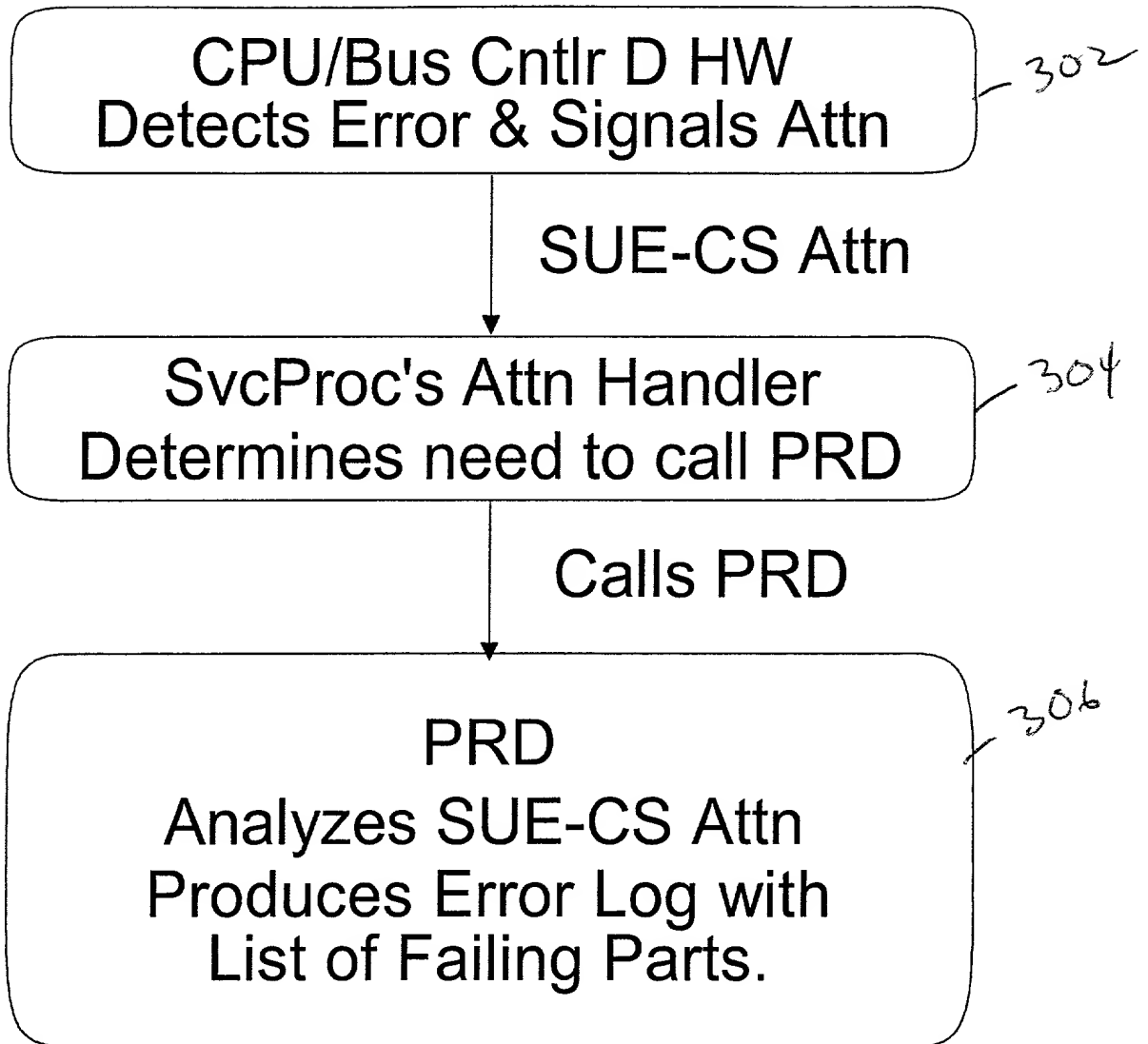


Figure 5

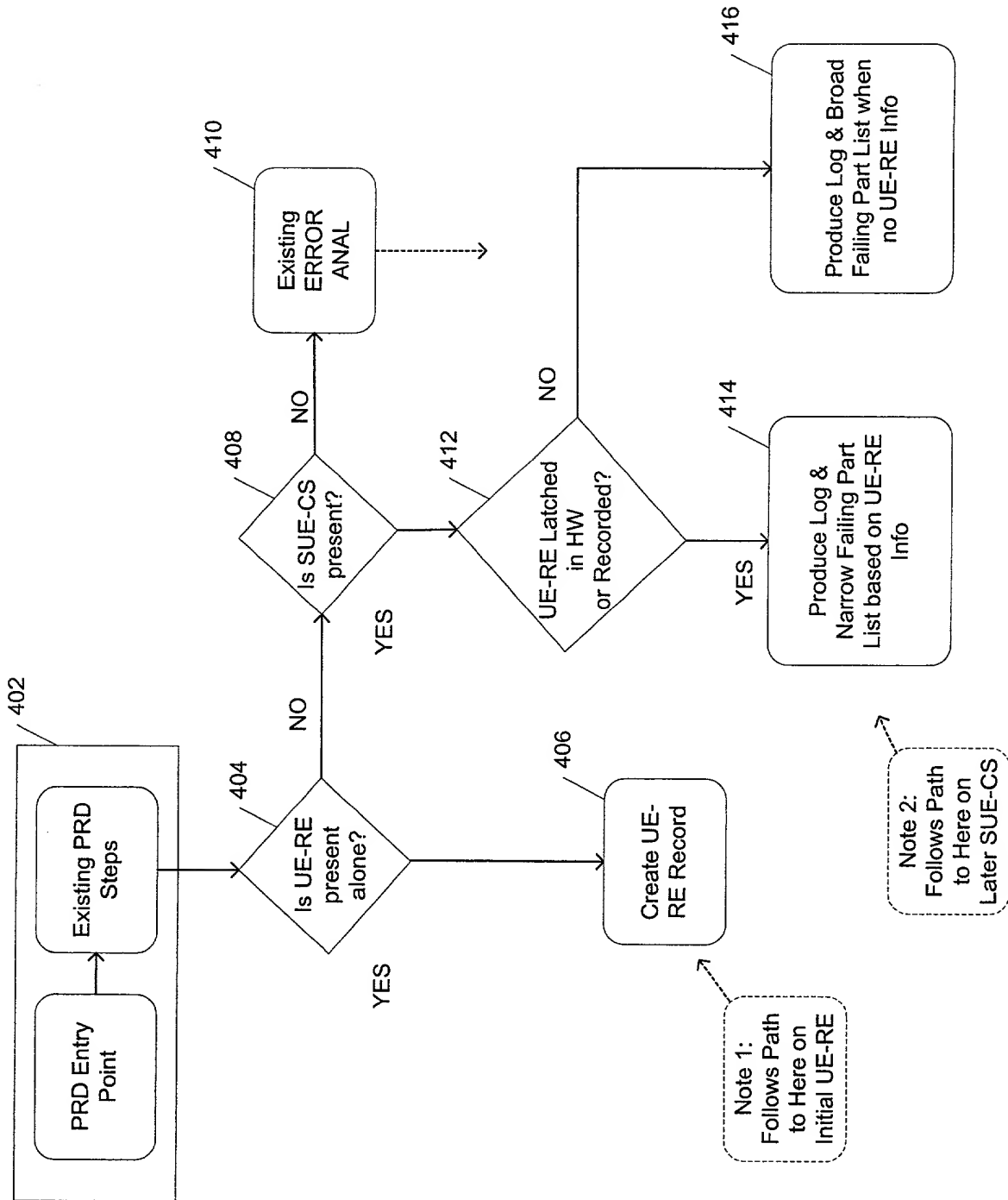


Figure 6

Figure 7

